

1143

31000 U.S. PTO
09/930202



257	627
Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. SCANNED <i>Cap</i> O.A. <i>NA</i>	PATENT DATE <i>APR 26 2004</i>
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APPLICATION NO. 09/930202	CONT/PRIOR F	CLASS 257	SUBCLASS <i>627</i>	ART UNIT <i>2415</i>	EXAMINER <i>1</i>
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APPLICANTS
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TITLE
Semiconductor wafer

PTO-2040
12/99

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ISSUING CLASSIFICATION							
ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
257	627	257	347	622	521		
INTERNATIONAL CLASSIFICATION		438	149	474	517		
H01L	27/01						
H01L	29/00						
H01L	29/06						

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg. <i>11</i>	Figs. Drwg. <i>21</i>	Print Fig. <i>1</i>	Total Claims <i>10</i>	Print Claim for O.G. <i>1</i>
The term of this patent subsequent to _____ (date) has been disclaimed.	<i>JOSEPH NGUYEN</i> (Assistant Examiner)			NOTICE OF ALLOWANCE MAILED <i>12/16/03</i>	
The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	<i>GEORGE ECKERT</i> PRIMARY EXAMINER (Primary Examiner)			ISSUE FEE (W) Amount Due <i>\$1650.00</i> Date Paid <i>2/26/04</i>	
The terminal _____ months of this patent have been disclaimed.	<i>Surber</i> <i>12/15/03</i> (Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER _____	
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1500-FILE 10/10/03